

The listing of claims will replace all prior versions, and listings, of claims in the application:

**Listing of Claims:**

1. (Currently Amended) A display device comprising:  
a source signal line driving circuit;  
a pixel portion;  
a shift register included in said source signal line driving circuit for outputting a pulse in accordance with clock signals;  
a level shifter included in said source signal line driving circuit for converting a voltage amplitude of input signals; and  
a current source for supplying a current to said level shifter based on the pulse from the shift register,  
wherein [[only]] when said shift register serially outputs the pulses, said current source supplies the current and said level shifter is operated.
2. (Previously Presented) A display device according to claim 1, wherein said source signal line driving circuit and said pixel portion are provided over a member selected from the group consisting of a glass substrate, a plastic substrate, a stainless steel substrate and a single crystal wafer.
3. (Previously Presented) A display device according to claim 1, wherein said driving circuit and said pixel portion are provided over a same substrate.
4. (Previously Presented) A display device according to claim 1, wherein said driving circuit and said pixel portion are provided over different substrates.

wherein [[only]] when said plurality of shift registers in said a-th unit serially outputs the pulses, said a-th current source supplies the current and said level shifters are operated.

11. (Previously Presented) A display device according to claim 10, wherein said source signal line driving circuit and said pixel portion are provided over a member selected from the group consisting of a glass substrate, a plastic substrate, a stainless steel substrate and a single crystal wafer.

12. (Previously Presented) A display device according to claim 10, wherein said driving circuit and said pixel portion are provided over a same substrate.

13. (Previously Presented) A display device according to claim 10, wherein said driving circuit and said pixel portion are provided over different substrates.

14. (Previously Presented) A display device according to claim 10, wherein said display device is a liquid crystal display device.

15. (Previously Presented) A display device according to claim 10, wherein said display device is incorporated into a personal computer.

16. (Previously Presented) A display device according to claim 10, wherein said display device is incorporated into a portable information terminal.

17. (Previously Presented) A display device according to claim 10, wherein said display device is incorporated into a car audio set.

18. (Previously Presented) A display device according to claim 10, wherein said display device is incorporated into a digital camera.

19-36. (Canceled)

37. (Currently Amended) A display device comprising:

a gate signal line driving circuit;

a pixel portion;

a shift register included in said gate signal line driving circuit for outputting a pulse in accordance with clock signals;

a level shifter included in said gate signal line driving circuit for converting a voltage amplitude of input signals; and

a current source for supplying a current to said level shifter based on the pulse from the shift register,

wherein [[only]] when said shift register serially outputs the pulses, said current source supplies the current and said level shifter is operated.

38. (Previously Presented) A display device according to claim 37, wherein said gate signal line driving circuit and said pixel portion are provided over a member selected from the group consisting of a glass substrate, a plastic substrate, a stainless steel substrate and a single crystal wafer.

39. (Previously Presented) A display device according to claim 37, wherein said driving circuit and said pixel portion are provided over a same substrate.

40. (Previously Presented) A display device according to claim 37, wherein said driving circuit and said pixel portion are provided over different substrates.

41. (Previously Presented) A display device according to claim 37, wherein said display device is a liquid crystal display device.

42. (Previously Presented) A display device according to claim 37, wherein said display device is incorporated into a personal computer.

43. (Previously Presented) A display device according to claim 37, wherein said display device is incorporated into a portable information terminal.

44. (Previously Presented) A display device according to claim 37, wherein said display device is incorporated into a car audio set.

45. (Previously Presented) A display device according to claim 37, wherein said display device is incorporated into a digital camera.

46. (Currently Amended) A display device comprising:  
a gate signal line driving circuit;  
a pixel portion;  
first to y-th ( $y$ : natural number,  $y \geq 2$ ) units included in said gate signal line driving circuit;  
a plurality of shift registers included in the d-th ( $d$ : natural number,  $1 \leq d \leq y$ ) unit for outputting a pulse in accordance with clock signals;  
a plurality of level shifters included in said d-th unit for converting a voltage amplitude of input signals; and  
a d-th current source for supplying a current to said plurality of level shifters based on the pulse from the shift registers,

5. (Previously Presented) A display device according to claim 1, wherein said display device is a liquid crystal display device.

6. (Previously Presented) A display device according to claim 1, wherein said display device is incorporated into a personal computer.

7. (Previously Presented) A display device according to claim 1, wherein said display device is incorporated into a portable information terminal.

8. (Previously Presented) A display device according to claim 1, wherein said display device is incorporated into a car audio set.

9. (Previously Presented) A display device according to claim 1, wherein said display device is incorporated into a digital camera.

10. (Currently Amended) A display device comprising:

a source signal line driving circuit;

a pixel portion;

first to x-th (x: natural number,  $x \geq 2$ ) units included in said source signal line driving circuit;

a plurality of shift registers included in the a-th (a: natural number,  $1 \leq a \leq x$ ) unit for outputting a pulse in accordance with clock signals;

a plurality of level shifters included in said a-th unit for converting a voltage amplitude of input signals; and

an a-th current source provided for supplying a current to said plurality of level shifters based on the pulse from the shift registers,

wherein [[only]] when said plurality of shift registers in said d-th unit serially outputs the pulses, said d-th current source supplies the current and said level shifters are operated.

47. (Previously Presented) A display device according to claim 46, wherein said gate signal line driving circuit and said pixel portion are provided over a member selected from the group consisting of a glass substrate, a plastic substrate, a stainless steel substrate and a single crystal wafer.

48. (Previously Presented) A display device according to claim 46, wherein said driving circuit and said pixel portion are provided over a same substrate.

49. (Previously Presented) A display device according to claim 46, wherein said driving circuit and said pixel portion are provided over different substrates.

50. (Previously Presented) A display device according to claim 46, wherein said display device is a liquid crystal display device.

51. (Previously Presented) A display device according to claim 46, wherein said display device is incorporated into a personal computer.

52. (Previously Presented) A display device according to claim 46, wherein said display device is incorporated into a portable information terminal.

53. (Previously Presented) A display device according to claim 46, wherein said display device is incorporated into a car audio set.

54. (Previously Presented) A display device according to claim 46, wherein said display device is incorporated into a digital camera.

55-72. (Canceled)

73. (Currently Amended) A display device comprising:  
a source signal line driving circuit;  
a pixel portion;  
a decoder included in said source signal line driving circuit for outputting a pulse in accordance with input signals;  
a level shifter included in said source signal line driving circuit for converting a voltage amplitude of the input signals; and  
a current source for supplying a current to said level shifter based on the pulse from the decoder,  
wherein [[only]] when said decoder serially outputs the pulses, said current source supplies the current and said level shifter is operated.

74. (Previously Presented) A display device according to claim 73, wherein said source signal line driving circuit and said pixel portion are provided over a member selected from the group consisting of a glass substrate, a plastic substrate, a stainless steel substrate and a single crystal wafer.

75. (Previously Presented) A display device according to claim 73, wherein said driving circuit and said pixel portion are provided over a same substrate.

76. (Previously Presented) A display device according to claim 73, wherein said driving circuit and said pixel portion are provided over different substrates.

77. (Previously Presented) A display device according to claim 73, wherein said display device is a liquid crystal display device.

78. (Previously Presented) A display device according to claim 73, wherein said display device is incorporated into a personal computer.

79. (Previously Presented) A display device according to claim 73, wherein said display device is incorporated into a portable information terminal.

80. (Previously Presented) A display device according to claim 73, wherein said display device is incorporated into a car audio set.

81. (Previously Presented) A display device according to claim 73, wherein said display device is incorporated into a digital camera.

82. (Currently Amended) A display device comprising:

a source signal line driving circuit;

a pixel portion;

first to x-th ( $x$ : natural number,  $x \geq 2$ ) units included in said source signal line driving circuit;

a plurality of decoders included in the a-th ( $a$ : natural number,  $1 \leq a \leq x$ ) unit for outputting a pulse in accordance with input signals;

a plurality of level shifters included in said a-th unit for converting a voltage amplitude of the input signals; and

an a-th current source for supplying a current to said plurality of level shifters based on the pulse from the decoders,



wherein [[only]] when said plurality of decoders in said a-th unit serially outputs the pulses, said a-th current source supplies the current and said level shifters are operated.

83. (Previously Presented) A display device according to claim 82, wherein said source signal line driving circuit and said pixel portion are provided over a member selected from the group consisting of a glass substrate, a plastic substrate, a stainless steel substrate and a single crystal wafer.

84. (Previously Presented) A display device according to claim 82, wherein said driving circuit and said pixel portion are provided over a same substrate.

85. (Previously Presented) A display device according to claim 82, wherein said driving circuit and said pixel portion are provided over different substrates.

86. (Previously Presented) A display device according to claim 82, wherein said display device is a liquid crystal display device.

87. (Previously Presented) A display device according to claim 82, wherein said display device is incorporated into a personal computer.

88. (Previously Presented) A display device according to claim 82, wherein said display device is incorporated into a portable information terminal.

89. (Previously Presented) A display device according to claim 82, wherein said display device is incorporated into a car audio set.

90. (Previously Presented) A display device according to claim 82, wherein said display device is incorporated into a digital camera.

91-108. (Canceled)

109. (Currently Amended) A display device comprising:

a gate signal line driving circuit;

a pixel portion;

a decoder included in said gate signal line driving circuit for outputting a pulse in accordance with input signals;

a level shifter included in said gate signal line driving circuit for converting a voltage amplitude of the input signals; and

a current source provided for supplying a current to said level shifter based on the pulse from the decoder,

wherein [[only]] when said decoder serially outputs the pulses, said current source supplies the current and said level shifter is operated.

110. (Previously Presented) A display device according to claim 109, wherein said gate signal line driving circuit and said pixel portion are provided over a member selected from the group consisting of a glass substrate, a plastic substrate, a stainless steel substrate and a single crystal wafer.

111. (Previously Presented) A display device according to claim 109, wherein said driving circuit and said pixel portion are provided over a same substrate.

112. (Previously Presented) A display device according to claim 109, wherein said driving circuit and said pixel portion are provided over different substrates.

113. (Previously Presented) A display device according to claim 109, wherein said display device is a liquid crystal display device.

114. (Previously Presented) A display device according to claim 109, wherein said display device is incorporated into a personal computer.

115. (Previously Presented) A display device according to claim 109, wherein said display device is incorporated into a portable information terminal.

116. (Previously Presented) A display device according to claim 109, wherein said display device is incorporated into a car audio set.

117. (Previously Presented) A display device according to claim 109, wherein said display device is incorporated into a digital camera.

118. (Currently Amended) A display device comprising:  
a gate signal line driving circuit;  
a pixel portion;  
first to y-th ( $y$ : natural number,  $y \geq 2$ ) units included in said gate signal line driving circuit;  
a plurality of decoders included in the d-th ( $d$ : natural number,  $1 \leq d \leq y$ ) unit for outputting a pulse in accordance with input signals;  
a plurality of level shifters included in said d-th unit for converting a voltage amplitude of the input signals; and  
a d-th current source provided for supplying a current to said plurality of level shifters based on the pulse from the decoders,

wherein [[only]] when said plurality of decoders in said d-th unit serially outputs the pulses, said d-th current source supplies the current and said level shifters are operated.

119. (Previously Presented) A display device according to claim 118, wherein said gate signal line driving circuit and said pixel portion are provided over a member selected from the group consisting of a glass substrate, a plastic substrate, a stainless steel substrate and a single crystal wafer.

120. (Previously Presented) A display device according to claim 118, wherein said driving circuit and said pixel portion are provided over a same substrate.

121. (Previously Presented) A display device according to claim 118, wherein said driving circuit and said pixel portion are provided over different substrates.

122. (Previously Presented) A display device according to claim 118, wherein said display device is a liquid crystal display device.

123. (Previously Presented) A display device according to claim 118, wherein said display device is incorporated into a personal computer.

124. (Previously Presented) A display device according to claim 118, wherein said display device is incorporated into a portable information terminal.

125. (Previously Presented) A display device according to claim 118, wherein said display device is incorporated into a car audio set.

126. (Previously Presented) A display device according to claim 118, wherein said display device is incorporated into a digital camera.

127-144. (Canceled)

145. (Currently Amended) A semiconductor device comprising:  
a driving circuit;  
a shift register included for outputting a pulse in accordance with clock signals;  
a level shifter included for converting a voltage amplitude of input signals; and  
a current source provided for supplying a current to said level shifter based on the pulse from the shift register,

wherein [[only]] when said shift register serially outputs the pulses, said current source supplies the current and said level shifter is operated.

146. (Previously Presented) A semiconductor device according to claim 145, wherein said driving circuit is provided over a member selected from the group consisting of a glass substrate, a plastic substrate, a stainless steel substrate and a single crystal wafer.

147. (Previously Presented) A semiconductor device according to claim 145, wherein said semiconductor device is a liquid crystal display device.

148. (Previously Presented) A semiconductor device according to claim 145, wherein said display device is incorporated into a personal computer.

149. (Previously Presented) A semiconductor device according to claim 145, wherein said display device is incorporated into a portable information terminal.

150. (Previously Presented) A semiconductor device according to claim 145, wherein said display device is incorporated into a car audio set.

151. (Previously Presented) A semiconductor device according to claim 145, wherein said display device is incorporated into a digital camera.

152. (Currently Amended) A semiconductor device comprising:  
a driving circuit;  
first to x-th ( $x$ : natural number,  $x \geq 2$ ) units;  
a plurality of shift registers included in the a-th ( $a$ : natural number,  $1 \leq a \leq x$ ) unit for outputting a pulse in accordance with clock signals;  
a plurality of level shifters included in said a-th unit for converting a voltage amplitude of input signals; and  
an a-th current source for supplying a current to said plurality of level shifters based on the pulse from the shift registers,  
wherein [[only]] when said plurality of shift registers in said a-th unit serially outputs the pulses, said a-th current source supplies the current and said level shifters are operated.

153. (Previously Presented) A semiconductor device according to claim 152, wherein said driving circuit is provided over a member selected from the group consisting of a glass substrate, a plastic substrate, a stainless steel substrate and a single crystal wafer.

154. (Previously Presented) A semiconductor device according to claim 152, wherein said semiconductor device is a liquid crystal display device.

155. (Previously Presented) A semiconductor device according to claim 152, wherein said display device is incorporated into a personal computer.

156. (Previously Presented) A semiconductor device according to claim 152, wherein said display device is incorporated into a portable information terminal.

157. (Previously Presented) A semiconductor device according to claim 152, wherein said display device is incorporated into a car audio set.

158. (Previously Presented) A semiconductor device according to claim 152, wherein said display device is incorporated into a digital camera.

159. (Currently Amended) A semiconductor device comprising:  
a driving circuit;  
a decoder included in said driving circuit for outputting a pulse in accordance with input signals;  
a level shifter included in said driving circuit for converting a voltage amplitude of the input signals; and  
a current source provided for supplying a current to said level shifter based on the pulse from the decoder,  
wherein [[only]] when said decoder serially outputs the pulses, said current source supplies the current and said level shifter is operated.

160. (Previously Presented) A semiconductor device according to claim 159, wherein said driving circuit is provided over a member selected from the group consisting of a glass substrate, a plastic substrate, a stainless steel substrate and a single crystal wafer.

161. (Previously Presented) A semiconductor device according to claim 159, wherein said semiconductor device is a liquid crystal display device.

162. (Previously Presented) A semiconductor device according to claim 159, wherein said display device is incorporated into a personal computer.

163. (Previously Presented) A semiconductor device according to claim 159, wherein said display device is incorporated into a portable information terminal.

164. (Previously Presented) A semiconductor device according to claim 159, wherein said display device is incorporated into a car audio set.

165. (Previously Presented) A semiconductor device according to claim 159, wherein said display device is incorporated into a digital camera.

166. (Currently Amended) A semiconductor device comprising:  
a driving circuit;  
first to x-th ( $x$ : natural number,  $x \geq 2$ ) units included in said driving circuit;  
a plurality of decoders included in the a-th ( $a$ : natural number,  $1 \leq a \leq x$ ) unit for outputting a pulse in accordance with input signals;  
a plurality of level shifters included in said a-th unit for converting a voltage amplitude of the input signals; and  
an a-th current source provided for supplying a current to said plurality of level shifters based on the pulse from the decoders,  
wherein [[only]] when said plurality of decoders in said a-th unit serially outputs the pulses, said a-th current source supplies the current and said level shifters are operated.



167. (Previously Presented) A semiconductor device according to claim 166, wherein said driving circuit is provided over a member selected from the group consisting of a glass substrate, a plastic substrate, a stainless steel substrate and a single crystal wafer.

168. (Previously Presented) A semiconductor device according to claim 166, wherein said semiconductor device is a liquid crystal display device.

169. (Previously Presented) A semiconductor device according to claim 166, wherein said display device is incorporated into a personal computer.

170. (Previously Presented) A semiconductor device according to claim 166, wherein said display device is incorporated into a portable information terminal.

171. (Previously Presented) A semiconductor device according to claim 166, wherein said display device is incorporated into a car audio set.

172. (Previously Presented) A semiconductor device according to claim 166, wherein said display device is incorporated into a digital camera.

173. (Previously Presented) A display device according to claim 1, wherein said source signal line driving circuit comprises thin film transistors.

174. (Previously Presented) A display device according to claim 10, wherein said source signal line driving circuit comprises thin film transistors.

175. (Previously Presented) A display device according to claim 37, wherein said gate signal line driving circuit comprises thin film transistors.

176. (Previously Presented) A display device according to claim 46, wherein said gate signal line driving circuit comprises thin film transistors.

177. (Previously Presented) A display device according to claim 73, wherein said source signal line driving circuit comprises thin film transistors.

178. (Previously Presented) A display device according to claim 82, wherein said source signal line driving circuit comprises thin film transistors.

179. (Previously Presented) A display device according to claim 109, wherein said gate signal line driving circuit comprises thin film transistors.

180. (Previously Presented) A display device according to claim 118, wherein said gate signal line driving circuit comprises thin film transistors.

181. (Previously Presented) A semiconductor device according to claim 145, wherein said driving circuit comprises thin film transistors.

182. (Previously Presented) A semiconductor device according to claim 152, wherein said driving circuit comprises thin film transistors.

183. (Previously Presented) A semiconductor device according to claim 159, wherein said driving circuit comprises thin film transistors.

184. (Previously Presented) A semiconductor device according to claim 166, wherein said driving circuit comprises thin film transistors.

185. (New) A semiconductor device comprising:

a level shifter;  
a current source which supplies a current to said level shifter on input of a pulse;  
and  
a latch circuit into which a signal is inputted through said level shifter.

186. (New) A display device comprising:  
a level shifter;  
a current source which supplies a current to said level shifter on input of a pulse;  
and  
a latch circuit into which an image signal is inputted through said level shifter,  
wherein said image signal is written into a pixel.

187. (New) A semiconductor device comprising:  
a level shifter;  
a current source which supplies a current to said level shifter on input of a pulse;  
a first latch circuit into which a signal is inputted through said level shifter; and  
a second latch circuit into which output of said first latch circuit is inputted.

188. (New) A display device comprising:  
a level shifter;  
a current source which supplies a current to said level shifter on input of a pulse;  
a first latch circuit into which a signal is inputted through said level shifter; and  
a second latch circuit into which output of said first latch circuit is inputted,  
wherein said signal is written into a pixel.